



VIGNAN'S

Foundation for Science, Technology & Research

(Deemed to be University)

-Estd. u/s 3 of UGC Act 1956

Department of Electronics and Communication Engineering

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CERTIFICATE

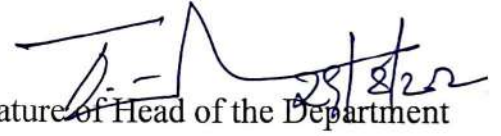
This is to certify that the major project report entitled “**Design of a low power high speed true single-phase clock D-flipflop for vernier time-to-digital converters**” that is being submitted by **K.Leela Sravani** bearing **Regd.No: 201FB11001** in partial fulfilment for the award of **M.Tech Degree in VLSI DESIGN** in **Vignan’s Foundation for Science Technology and Research (Deemed to be University)** is a record of bonafide work carried out by her under the guidance of **Dr. P.Vijaya Lakshmi**, Asst.Professor of ECE Department.



Signature of the faculty guide

Dr. P.Vijaya Lakshmi

Asst.Professor, ECE



Signature of Head of the Department

Dr.T.Pitchaiah

Professor & HOD, ECE


Professor & Head
Dept. of Electronics & Communication Engineering
VIGNAN'S FOUNDATION
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(Declared to be Deemed University U/S 3 of UGC Act 19.
VADLAMUDI - 522 213.
Guntur (Dt.), A.P., INDIA



Signature of external examiner


CERTIFICATE

This is to certify that the major project report entitled “**Design of CNTFET-Based Quaternary ALU Using 2:1 Multiplexer Based Approach**” that is being submitted by **P.Durga Vasavi** bearing **Regd.No: 201FB11002** in partial fulfilment for the award of M.Tech Degree in VLSI Design in Vignan’s Foundation for Science Technology and Research (Deemed to be University) is a record of bonafide work carried out by her under the guidance of **Dr. M. Sarada**, Professor of ECE Department.


Signature of the faculty guide

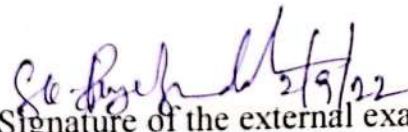
Dr. M. SARADA

(Professor, ECE)


Signature of Head of the Department

Dr.T.Pitchaiah

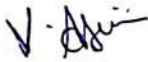
(Professor & HOD, ECE)


Signature of the external examiner

Professor & Head
Dept. of Electronics & Communication Engineering
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VADLAMUDI - 522 213.
Guntur (Dt.), A.P., INDIA

CERTIFICATE

This is to certify that the internship report entitled “**Testing of Digital Designs Using Design For Testability**” that is being submitted by **A.Swapna Madhuri** bearing **Regd.No: 201FB11003** in partial fulfilment for the award of M.Tech Degree in VLSI Design in Vignan’s Foundation for Science Technology and Research (Deemed to be University) is a record of bonafide work carried out by her under the guidance of **V.Aswini**, Assistant Professor of ECE Department.



Signature of the faculty guide

V.ASWINI

(Assistant Professor, ECE)



Signature of Head of the Department

Dr.T.Pitchaiah

(Professor & HOD, ECE)

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Dept. of Electronics & Communication Engineering
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(Declared to be Deemed University U/S 3 of UGC Act 1956)
VADLAMUDI - 522 213.
Guntur (Dt.), A.P., INDIA



Signature of the external examiner

CERTIFICATE

This is to certify that the major project report entitled “**Low leakage power accuracy-configurable radix-4 adder using Integrated Drain Lector Technique**” that is being submitted by **T.Gowthami** bearing **Regd.No: 201FB11004** in partial fulfilment for the award of M.Tech Degree in VLSI Design in Vignan’s Foundation for Science Technology and Research (Deemed to be University) is a record of bonafide work carried out by her under the guidance of **Mr. M. A. Azeem**, Asst.Professor of ECE Department and under the co-guidance of **Dr.M.Sarada**, Professor of ECE Department.

Signature of the faculty guide

Mr. M. A. Azeem

(Asst. Professor, ECE)

Signature of Head of the Department

Dr.T.Pitchaiah

Professor & Head

Dept. of Electronics & Communication Engineering
(Professor & HOD, ECE)

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Guntur (Dt.), A.P., INDIA

Signature of the faculty guide

Dr.M.Sarada

(Professor, ECE)

Signature of the external examiner

CERTIFICATE

This is to certify that the major project report entitled “**Design & Implementation of Radiation Borne Error Resistant Flip Flop**” that is being submitted by **P.Divya** bearing **Registration Number 201FB11005** in partial fulfilment for the award of M.Tech Degree in VLSI Design in Vignan’s Foundation for Science Technology and Research (Deemed to be University) is a record of bonafide work carried out by her under the guidance of **Dr. Usha Rani .Nelakuditi**, Professor of ECE Department.

Signature of the faculty guide



Dr. USHA RANI. N

(Professor, ECE)

Signature of Head of the Department



Dr.T.Pitchaiah

(Professor & HOD, ECE)

Professor & Head

Dept. of Electronics & Communication Engineering

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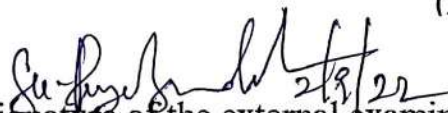
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Guntur (Dt.), A.P., INDIA


Signature of the external examiner



CERTIFICATE

This is to certify that the major project report entitled “**DESIGN OF ENERGY EFFICIENT HIGH-SPEED 1-BIT HYBRID FULL ADDER FOR FAST COMPUTATION**” in cadence that is being submitted by **VESAPAGA GRACE NISSI** bearing 201FB11006 in partial fulfilment for the award of M.Tech Degree in Electronics and Communication Engineering Under the course of **VERY LARGE SCALE INTEGRATION DESIGN** to Vignan’s Foundation for Science Technology and Research, is a record of work carried out by him/her under the guidance of ECE Department.


Signature of the faculty guide
DR. SATYAJEET SAHOO
(Assistant Professor, ECE)


Signature of Head of the Department
Dr. T. PITCHAIAH
(Professor & HOD, ECE)
Professor & Head
Dept. of Electronics & Communication Engineering
VIGNAN'S FOUNDATION
FOR SCIENCE, TECHNOLOGY AND RESEARCH
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VADLAMUDI - 522 213.
Guntur (Dt.), A.P., INDIA


Signature of the External Examiner

CERTIFICATE

This is to certify that the internship report entitled “**Testing of Digital Designs Using Design For Testability**” that is being submitted by **Ms. Mohammad Shama Parveen** bearing **Regd. No. 201FB11007** in partial fulfillment for the award of M.Tech degree in **VLSI DESIGN** to the Vignan’s Foundation for Science, Technology and Research, Deemed to be University, is a record of bonafide work carried out by her at **Tessolve Semiconductor Pvt. Ltd** under the supervision of **Mr. Ramji Damodaran** and the internal guidance of **K. Hima Bindu** of the ECE Department.


Signature of the faculty Guide

Mrs. K. Hima Bindu, M.Tech,(Ph.D)
Assistant Professor of ECE
VFSTR, Vadlamdui


Signature of the HOD

Dr. T. Pitchaiah
Professor & HOD of ECE

VFSTR, Vadlamdui
Dept. of Electronics & Communication Engineering
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VADLAMUDI - 522 213.
Guntur (Dt.), A.P., INDIA



Signature of the External Examiner

CERTIFICATE

This is to certify that the internship report entitled “Testing of Digital Designs Using Design For Testability” that is being submitted by **K.Chandana Tejaswini** bearing **Regd.No: 201FB11008** in partial fulfilment for the award of M.Tech Degree in VLSI Design in Vignan’s Foundation for Science Technology and Research (Deemed to be University) is a record of bonafide work carried out by her under the guidance of **Md.Taj**, Assistant Professor of ECE Department.


Signature of the faculty guide

Mr Md.Taj M.tech(Ph.D)
Assistant Professor,
Dept of ECE,VFSTR.


Signature of Head of the Department

Dr.T.Pitchaiah
Professor & Head
Dept. of Electronics & Communication Engineering
Professor & HOD,
FOR SCIENCE, TECHNOLOGY AND RESEARCH
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VADLAMURU - 527 213
GUNTUR (D.T.), A.P., INDIA


Signature of the external examiner

CERTIFICATE

This is to certify that the internship report entitled "Verification Of Standalone PCIe Using Intel FPGA Cyclone 10GX Development Board" that is being submitted by Ms. **Mulpuri Divya** bearing Regd. No. **201FB11009** in partial fulfilment for the award of M.Tech degree in **VLSI DESIGN** to the Vignan's Foundation for Science, Technology and Research, (Deemed to be University), is a record of bonafide work carried out by her at **L&T Technology Services Limited** under the supervision of **Mr. Vijayendra K.N.** and the internal guidance of **Dr. T. Pitchaiah** of ECE Department.


Signature of the faculty Guide

Dr. T. Pitchaiah

Professor & HOD of ECE

VFSTR, Vadlamudi


Signature of the HOD

Dr. T. Pitchaiah

Professor & HOD of ECE

VFSTR, Vadlamudi


Signature of the External Examiner


Professor & Head
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